

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S30	665	socket same board same network	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 15:40
S31	418	S30 and power	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 15:41
S32	381	S30 and power and circuit\$3	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 15:41
S33	267	S30 and power and circuit\$3 and (memory or storage) and (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 15:45
S34	665	socket same board same network	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 18:20
S35	267	S34 and power and circuit\$3 and (memory or storage) and (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 18:20
S36	267	S35 and power and circuit\$3 and (memory or storage) and (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/23 18:20

S37	1145	circuit\$3 with board with (socket or slot) same (backplane or motherboard or midplane) and power and (memory or storage) and (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/23 18:23
S38	15	circuit\$3 with board with (socket or slot) same (backplane or motherboard or midplane) with power with (memory or storage) with (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/23 18:25
S39	23	(socket or slot) same (backplane or (motherboard or (circuit \$3 with board)) or midplane) with power with (memory or storage) with (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3) with network	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/23 18:32
S40	2	"6789146".pn.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 11:01
S41	2	"5971813".pn.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 11:45
S42	2	"7054165".pn.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 11:52
S43	2639	(socket or connector or slot) same (motherboard or (circuit \$3 near board)) with memory with (processor or controller or micoprocessor)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 12:55

S44	44	(socket or connector or slot) same (motherboard or (circuit \$3 near board)) with power with (memory or storage) with (control\$4 or microcontroller or cpu or process\$3 or microprocess\$3) with network	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 12:56
S45	9	("20010042167" "20020152339" "20020178143" "20020196601" "20030204671" "20040057203" "6325636" "6510050" "7003687").PN. OR ("7380057").URPN.	US_PGPUB; USPAT; USOCR	OR	OFF	2009/06/24 13:25
S46	1	"6827589".pn.	US_PGPUB; USPAT; USOCR	OR	OFF	2009/06/24 14:33
S47	4	("6301104").PN. OR ("6827589").URPN.	US_PGPUB; USPAT; USOCR	OR	OFF	2009/06/24 14:35
S48	136214	(motherboard or (circuit \$3 near board)).ti.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 15:46
S49	0	S48 and (socket or connector or slot) with power with (memory or storage) with (control\$4 or microcontroller or cpu or process\$3 or microprocess\$3) with network with (cable with line)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 15:48
S50	0	S48 and (socket or connector or slot) with network with (cable with line) and power with (memory or storage) with (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 15:48

S51	7	S48 and (socket or connector or slot) and network and (cable with line) and power with (memory or storage) with (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 15:49
S52	64	S48 and (socket or connector or slot) and network and power with (memory or storage) with (control \$4 or microcontroller or cpu or process\$3 or microprocess\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 16:13
S53	0	361/684.ccls.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 16:25
S54	0	361/686.ccls.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 16:25
S55	489	361/788.ccls.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 16:26
S56	27	361/788.ccls. and network and ("rj-45" or "rj45")	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/24 16:30
S57	2	"6301104".pn.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/25 14:49
S58	5	Kenneth Franzel.in.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	NEAR	OFF	2009/06/25 19:59
S59	3	S58 and socket same backplane same configur\$4 same network	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/25 19:59

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